



Call for Papers

DATE 2018 Friday Workshop on

Emerging Memory Solutions & Applications

Technology, Manufacturing, Architectures, Design, Automation and Test

Dresden, Germany

Friday March 23, 2018

<https://ems.eit.uni-kl.de/en/ems-workshop>

General Chair:

Said Hamdioui – TU Delft (NL)

Program Co-Chairs:

Bastien Giraud – CEA-LETI (FR)

Christian Weis – University of
Kaiserslautern (DE)

Panel Chair:

Ian O'Connor – INL (FR)

Publicity Chair:

Matthias Jung – Fraunhofer
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Proceeding Chair:

Jean-Philippe Noel – CEA-LETI
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Steering Committee Member:

Erik Jan Marinissen – IMEC (BE)

The **Design, Automation, and Test in Europe** conference and exhibition is the main European event bringing together researchers, vendors and specialists in hardware and software design, test and manufacturing of electronic circuits and systems. **Friday Workshops** are dedicated to emerging research and application topics. At DATE 2018, one of the Friday Workshops is devoted to **Emerging Memory Solutions & Applications**. This one-day event consists of two plenary keynotes, regular and poster presentations, and a panel session.

WORKSHOP DESCRIPTION

Memory manufacturing, architectures, design and test are deeply investigated to face issues linked to technology scaling such as increasing static power, maximum operating frequency and the gap between logic and memory minimum voltages. Various emerging memories solutions have appeared in recent years to replace either partially or totally already existing memories with an aim to overcome both technology and design related limitations in order to answer the requirements of many different applications. The goal of this Workshop is to bring together researchers, practitioners, and others interested in this exciting and rapidly evolving field, in order to update each other on the latest state-of-the-art, exchange ideas, and discuss future challenges.

TOPIC AREAS

You are invited to participate and submit your contributions to DATE 2018 Friday Workshop on **Emerging Memory Solutions & Applications**. The areas of interest include (but not limited to) the following topics:

- Volatile memory design (SRAM, DRAM, CAM, etc.)
- Non-Volatile memory design (ReRAM, Flash, PCM, MRAM, etc.)
- Applications of emerging devices in memories, such as TFETs, CNTs, nanowires, etc.
- 3D memories (volatile and non-volatile)
- Memory systems for Deep Learning applications
- Processing in Memory, Logic in Memory and In-memory computing
- Memory Application for emerging markets
- Memory test, BIST and debug techniques
- Applications, products, and prototypes of new memories

SUBMISSION INSTRUCTIONS

Submissions are invited in the form of (extended) abstracts not exceeding two pages and must be sent in as PDF file to <bastien.giraud@cea.fr> with "DATE18-EMS-WS" as subject. All submissions will be evaluated for selection with respect to their suitability for the workshop, originality, and technical quality. Selected submissions will be accepted for oral or poster presentation. At the workshop, an Electronic Workshop Digest will be made available to all workshop participants, which will include all materials that authors are willing to provide: abstract, paper, slides, poster, etc. Authors of the extended abstract are at liberty to submit a version of their work to other conferences and to journals without violating common codes of ethics. Selected works will be invited to submit a full paper to MEMSYS (www.memsys.io).

Paper Submission deadline
Notification of Acceptance
Camera-Ready Material due date

January 08, 2018
January 10, 2018
February 28, 2018

MORE INFORMATION

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